thanx to

NAM	- Akanksha Saxena	c	Date -	
COURS	E CODE - MCS - O4)	1	SEMESTER - 4xH.	6
	COURSE TITLE :	OPERATING	SYSTEMS	6
ROLL				
Ques 1				
Solution	:			
(a) Using the Shortest-Job-First method (SJF)				
110 the OTE and adding ashome because the subselled				
length of process well feet get wereness.				
chart will be				
Б	I D C			
0	4 8	12	19	
Because shockest earning time is of the Process B, then				
process D and then C and then A. The waiting time				
	vocas A is 13 m		ocess B is Ony,	for
- DI	e is some and	for pio	cess D is 4 ms. a	& -
process				
Time	Process Completed	/ doctrino		
0		1 0 0	4, -2 = 9	·
4	В	4-2=2	4-3=	Į.
8	D	8-5=3	10 - 5 =	
15	C	13-3 = 10		
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completion time = A = 52, B = 4, C = 15, D = 4 (b) easing Shortest Remains time first (SRTF) method A time o only process A has entered the system, so it is the process that executes. At time 2, process B arrives. At that theme, peoces A has 4 time units left to execute at this time quenction peocess B's processing time is same as strat of A is 4 So A continues executing At time 3 process centers the system with processing time 5 units when compared with percess A and B, the stime of A is 3 and B is 4 so A continues. A time 5 process D enters with remaining time of 4. At this time A is remaing with I execution time, B with 4, C with 5 and D with 4. lince C has hieghest time semaing, it is executed at end and since B and D have same execution, they will be peocused according the sequence they entered into the system So the gant chart will be: A 6 14 Turnaround time of each process can be computed by subtracting the time it terminated from arrival time waiting time A = 6 - 0 = 6 B = 10 - 2 = 8 C = 19 - 3 = 16 D = 14 - 5 = 9



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(C) Deing the Round Robin (RR) method Quantum - 2 milliseconds If we use a time Quantum of 2 milliseconds then process A gets the first 2 milliseconds - Since it require another 4 milliseconds, it is prempted after first time Quantum and the CPU is given to the next peocess in queux, Peocess B. Since. B is having 4 processing time, and it gets 2 milliseconds, it foil be prompted later to complete its of milliseconds. Now A is left with 4 me and B is left with 2 ms. The CPU a then given to the next procus (with 5 ms. Now after taking 2ms of CPU time, C is left with 3ms Then CPU is alloted to the next peocess in D, and after 2ms D is left with & ms. So, now at this time A is left with 4 ms, B with 2 ms, C with 3 me, Dwith 2 ms. They will be devided again with the quantum time The gaint chart? D B A C 2 4 10 12 14 16 18 PROCESSMY Waiting Trun Asome PROCESS Times Time 6-0=6 6 - 0 = 0 A 4 - 2 = 2 B D 4-(-1)=5 completion Time



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```
Bankers Algorithm
Section
# include < stdio.h>
# include (conio. h>
void moun () f
     int k=0, output[10], d=0, t=0, ins[5], i;
     int avail [5], allocated [10][5], need [10][5], MAX[10][5];
     int pro, P[10], j, 22, count=0;
     chico ();
    paintf ("In Enter the number of sesources")
     scanf (10/00", $ 27);
    paintf ("In Enter the max instance of each resources)
     for (1=0; i< 22; (++) }
          availti] = 0;
          paintf ( " 1.c = " , (1+97));
          scand (40/odt, fine[i]);
    painty ( "In Enter number of processes");
    scany ( " fod", & pno);
    points ( "In Enter the allocation matrix In ");
    for (i=0 ; i< 2 ; i++)
    peint ( 40/00", (i+97));
    peintf ("In");
    for ( i= 0; (< pno; i++) }
        P[[] = 13
        painty ("PE / d) ", PC);
        for (j=0; j< 2 ; j++) {
          scang ("of d", & allocated [i][j])
          avail GJ+ = allocated [i]GJi.
        WWW.VIJAY-JOTANI.WEEBLY.COM
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point ( "In thee the MAX mortis In ")
 for 11-0: (CAZ 3 (1+) }
      paint (40/00 , (1+97));
      avail [i] - ins [i] - avail[i];
 painty ("Int);
 for (1=0) (< pro )(++) }
       punty ("P[%d]", i);
       for 1j=0 = j < 2 = j++)
       scanf ( " of.d", &MAX CIDEJ );
paint (" \n");
A d=-13
for (i= o) (< proj i++ ) }
   Count = 0;
   t = P Cills
  for (j=0) j(2) j+) {
     need [t] [] = MAX[t] [] - allocated [][];
     if (need Ex) (j) <= avoil (j))
count+1;
  if ( count = = 12) }
      output (K++] = Pli];
     for (j=0; j<2; j++)
     avail (j) + = allocated (+)(j);
 } else
    PE++d] = PCi];
if (d1=-1) }
       pno = d+1
       goto A; }
paint ("It <");
    WWW.VIJAY-JOTANI
```



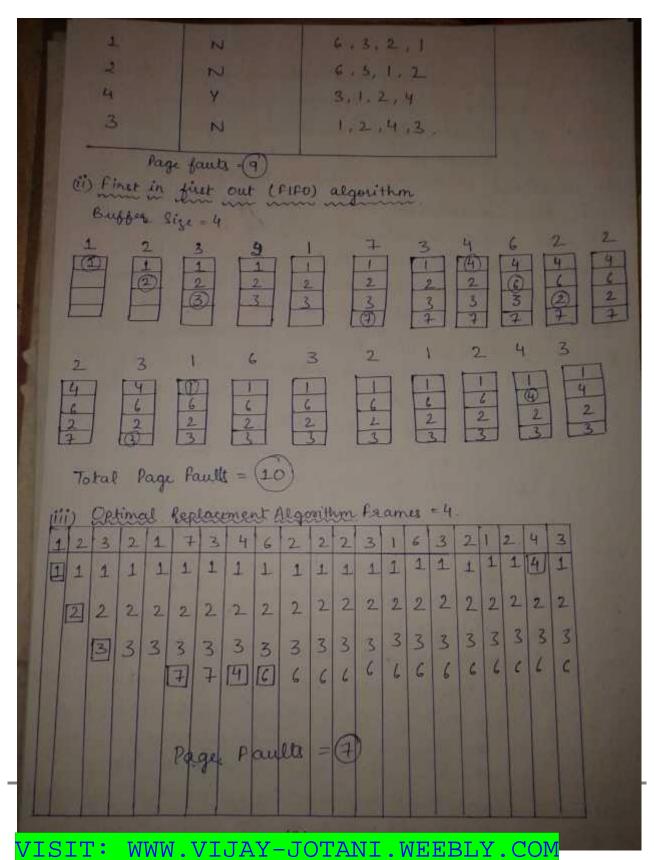
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for the or in kritt) punt (1>1) getch (); (6) Solution (i) Least Recently Verd (LRV) Algorithm Rage referencing string 1,2,3,2,1,7,3,4,6,2,2,2,3,1,6,3,2,1,2,4,3 Resulting list (Frame = 4) Page Pault Page Refrol 1 2 1,2 1,2,3 3,2,1 N 7 3, 2, 1, 7 3 2,1,7,3 N 1,7,3,4 7,3,4,6 6 3,4,6,2 2 3,4,6,2 2 N 3,4,6,2 2 N 4,6,2,3 3 N 6,2,3,1 1 7 2,3,1,6 N 6 2,1,6,3 N 1,6,3,2 N Y-JOTANI.WEEBLY



Akanksha Hi everyone, I am *Akanksha*. I am not sure if all the answers are correct this is all it could come up with. Please read carefully before writing, ram not responsible for anything thing. Thank you all.

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Pines 3) (9) The primary impact of disallowing paging of kunst memory in lineax is that the non-preemptability of the keenel is preserved. Any process taking a page fault, whether in keinel or in user mode, risk being se-scheduled while the required is paged in from olisk. Bengo wednestaked because the kernel can tely on not being rescheduled during access to its primary data structures, locking requirements to protect the integrity of those what a steuctures are very greately simplified. Although design simplicity is a benefit in itself it also prevides an important performance advantage on uniprocessor machines due to the fact that it is not necess away to do additional locking on most internal data structures. There are number of disadvantages to the lack of pageable keend memory, however. first of all. It imposes constraints on the amount of memory that the keinel can use . It is unleasonable to keep very large data structures on non pageable memory, since that represents physical memory that absolutely cannot be used for anything else. This has two impacts: first of all, the keinel must prure back many of its intunel data structures manually, instead of being able to very on a single victual - memory mechanism to keep physical memory useage under control. second, it make it infeasible to implement terrain features that beguire large amount of milies the ternel, such as the



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(b) solution: A process in windows 2000 is limited to 2 graphyte address space for data. The two-stage process allows the access of much larger dalabases datasets , by useurs space in in the processes address space first and then committing the storage to a memory morpheth file. An application could thus window theoryh a large database. Ques4 solution (1) Process Management in Windows 8.1 Windows & 1 operating system sievices allows creation of here processes in response to a system call. It allows users to create multiple numbers of process with default attributes and identifiers. Any peocess cannot start on its own but requires system calling. With the heation of new process, process control block is taken from the main memory that provides the parameter about the sunning beocess like process name process peroxity etc. You might have noticed that some process will not work because of wrong ID and no space for peoces control block calling. You can also destroy and remove the specified process from the computer system by responding to a system call. A newly created process can be semoned Windows 8 environment by using deleting Operating system



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sometimes, due to lack of resources the situation might over in which forceful termination of assesstey running process are sequered which is furfiled by about command which le a same as delete command 3) Memory Management - windows 8 consist of an advance westers memory management system It provides a number of finctions for using it and part of the executives and Six dedicated this keenel theraple for managing it. In windows & I each user peocess has its own virtual address pace which is 32 bit long. The lower 290 minus appear 256 mb are reserved for process's code and data; the upper 268 map onto to kernel memory in a protected way The medical address space is domand paged with fixed pages size. In general windows 2000 sesolves vassours Confeicts through complex heuristics questions, historical precedent, enles of thumb and admonstrator-controlled parameter setting. Memory management is a highly complex subsystem with many data structures, algorithm and heuristics 3) I/o management ! The I/o requests passes through several predictable stages of processing. The stages vary depending on whether the request is destined for a device operated by a single-layered driver of for a device reathed through multiplayedred dainer Thus. I/o types are · Synchionaus and asyncheconous I/O: Most 1/0 operations that applications use are synchronous; that is the application threads waits while the device performs the data operation and getters a status code when the I/O is complete. Assyncheonous Ho allows an application to issue mulipe. I/o requests and continue executing while the dente performs the 40 operations.



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fast I/o is a special mechanism that allows the I/O System to bypass generating an IRP and instead go directly to the driver stack to complete an 1/0 request 3) File management windows support several file systems like FAT-16, FAT-32, and NTFS. It also supports read array file segetems for CD-ROMS and DVDs. It is possible to have access to multiple file system lypes on the same eurning system. Windows 8 have few added new features for file management. Consolidated copy experience, Independent job management which gives ability to manage each job separately bry copy coj undurany can be paused, resumed or cancelled endependent of the others. 4) Security & Projection once usindows is sunning its harder to use the way it hardles memory that's in used to attack the DS or the programs your's running. Thus the cocept of Gavid pages was intereduced in windows 81. The keenel can put "gaus pages" of memory around important code like a most, so that if malwar teins to attack by currepting the next chenk of memory and overflowing it more likely to end up the memory most and windows will shut down the process for accusing invalid menony Memory that no longer needed is less vulnerable as well Maliar to be able to force the keenel to allocate memory to a program and then release it to that a views could eise memory that windows throught washit in use, tenat's now blocked windows 8.1 uses the repervisor mode Execution Protection (SMEP or OS gand) in My Bridge CPU to Stop the CPU running an

